

FIGURE 1

200

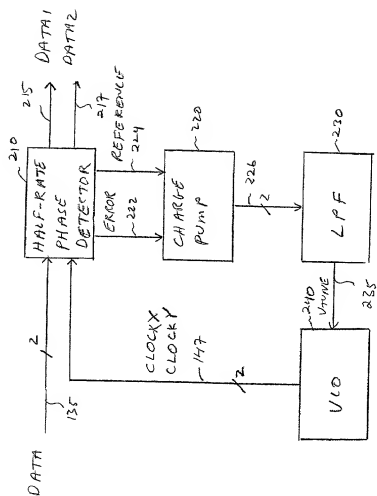
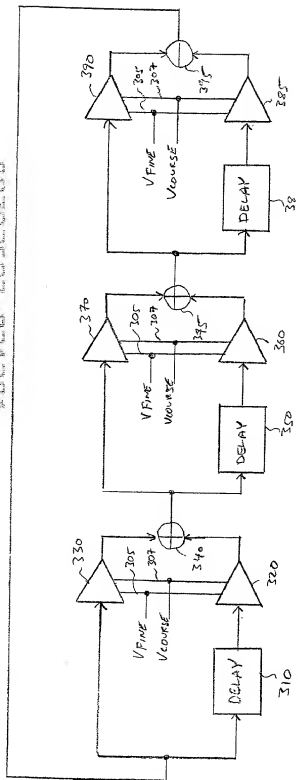


FIGURE 2



300

FIGURE 3

09702687-021001

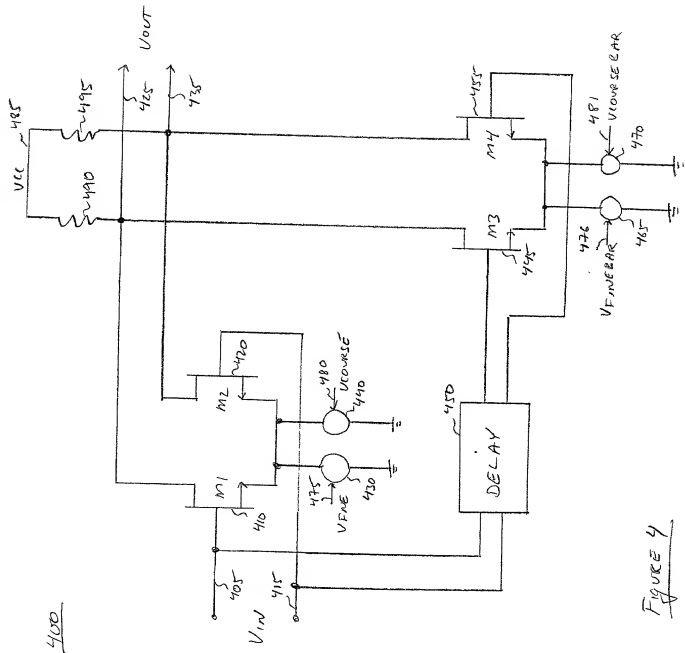


FIGURE 4

Q05

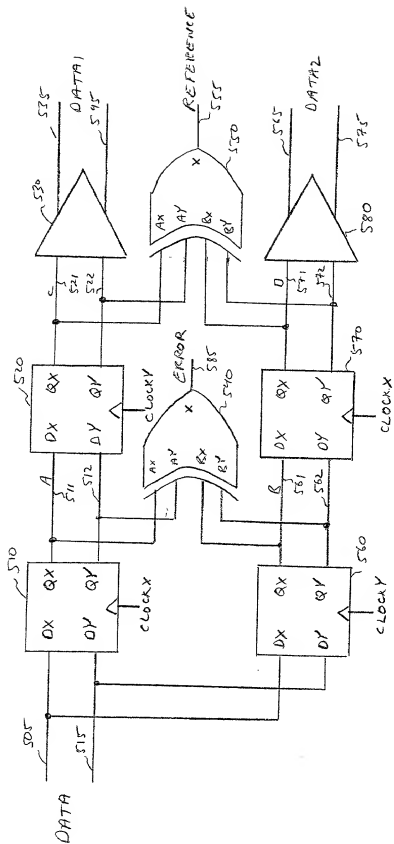


FIGURE 5

600

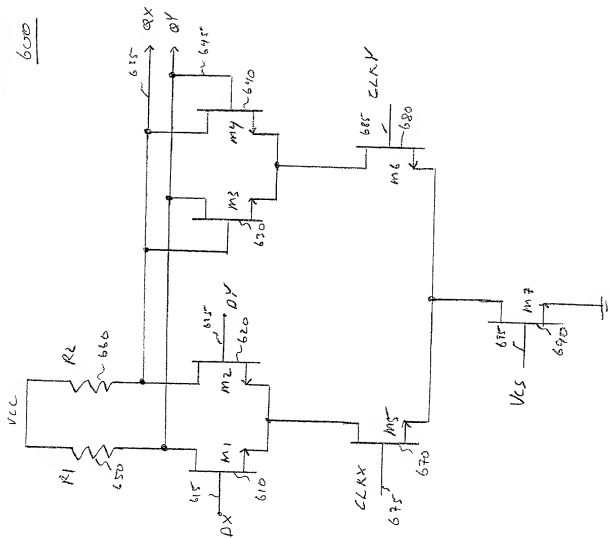


Figure 6



Figure 3

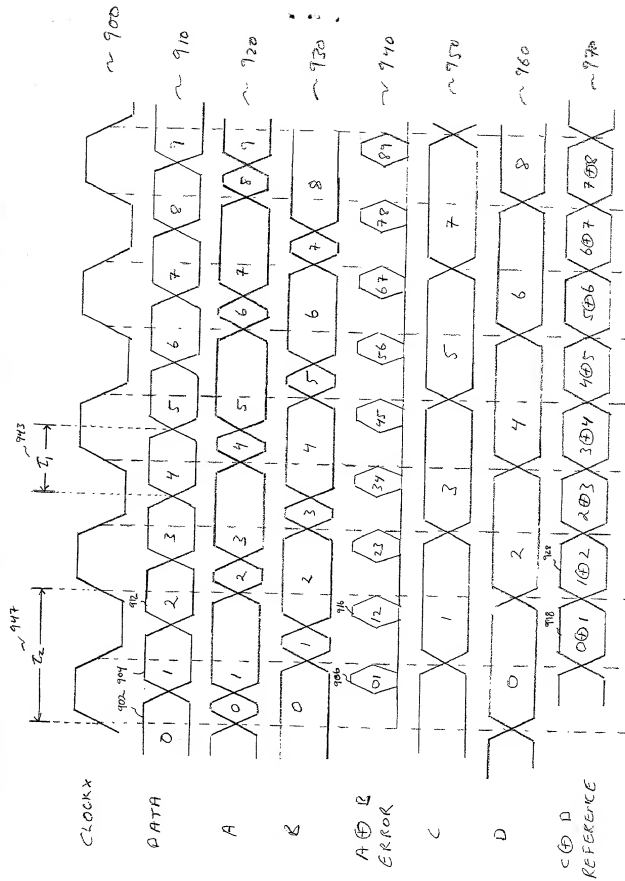


Figure 9

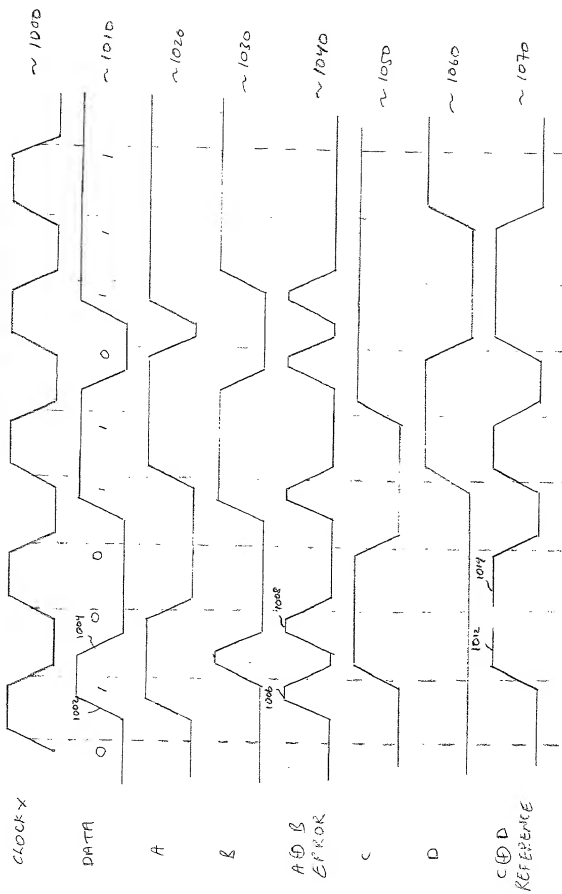


Figure 10

0978587 01400

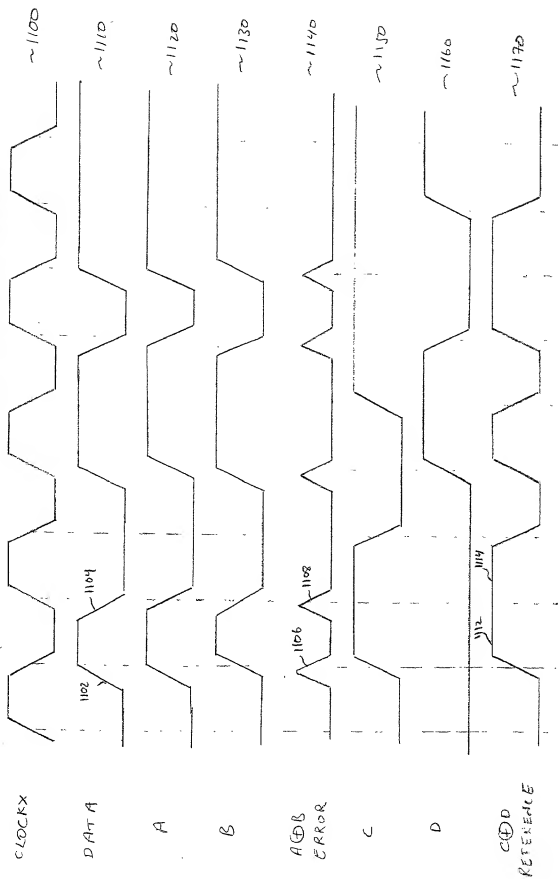


FIGURE 11

1200

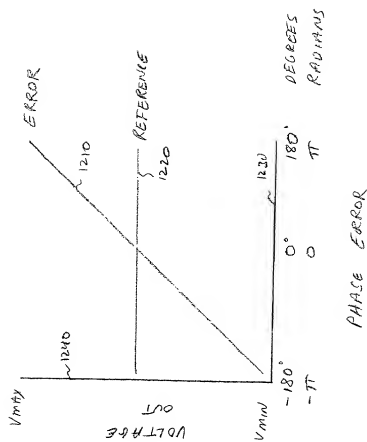


FIGURE 12

PROVIDE AN INPUT DATA SIGNAL, A CLOCK SIGNAL, AND A COMPLEMENTARY CLOCK SIGNAL.

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APPLY THE INPUT DATA TO A FIRST LATCH CLOCKED BY THE CLOCK SIGNAL.

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APPLY THE INPUT DATA TO A SECOND LATCH CLOCKED BY THE COMPLEMENTARY CLOCK SIGNAL.

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APPLY THE OUTPUT OF THE FIRST LATCH TO A FIRST XOR GATE AND A THIRD LATCH.

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APPLY THE OUTPUT OF THE SECOND LATCH TO THE FIRST XOR GATE AND A FOURTH LATCH.

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APPLY THE OUTPUT OF THE THIRD LATCH AND THE FOURTH LATCH TO A SECOND XOR GATE.

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USE THE OUTPUT OF THE FIRST XOR GATE AS AN ERROR SIGNAL, THE OUTPUT OF THE SECOND XOR GATE AS A REFERENCE SIGNAL, THE OUTPUT OF THE THIRD LATCH AS A FIRST DATA OUTPUT, AND THE OUTPUT OF THE FOURTH LATCH AS A SECOND DATA OUTPUT.

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SUBTRACT THE ERROR SIGNAL FROM $1/2$ THE REFERENCE SIGNAL, AND FILTER.

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13

USE FILTER OUTPUT TO ADJUST CLOCK AND COMPLEMENTARY CLOCK SIGNALS.

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